

**REMARKS**

Claims 14-28 are pending in this application. By this Amendment, non-elected claims 1-13 are canceled and claims 14 and 15 are amended. New claims 20-28 are added to recite additional features supported in the specification, for example, at least at Figs. 2, 3 and 5, and pages 9-14 and 17-19. No new matter is added. Reconsideration in view of the above amendments and following remarks is respectfully requested.

The Office Action objects to claim 15 asserting that the second "first" should be deleted before the term "voltage." To obviate this objection, claim 15 is amended to delete the second "first." Thus, Applicant respectfully requests that the objection to claim 15 is withdrawn.

The Office Action rejects claims 14-19 under 35 U.S.C. §102(e) as being anticipated by Kimura (U.S. Patent No. 6,781,567). Applicant respectfully traverses this rejection.

Kimura fails to disclose or suggest a method of driving an electro-optical device, including setting the potential of the control terminal to a second voltage level by using a capacitance coupling occurring at a capacitive element connected to the control terminal, the setting of the potential of the controlling terminal to the second voltage level including applying a data voltage to the capacitor through one data line of the data lines and a switching transistor, as recited in independent claim 14.

Kimura, in Figs. 1-4, discloses a method for implementing a gray-scale display of an electro-optical device according to a time ratio gray-scale method without providing reset lines. In col. 3, lines 34-61, Kimura discloses that a voltage VS corresponding to the on-signal causes the switching transistor to be in the on-state. A voltage VR corresponding to the on-signal causes the reset transistor to be in the on-state. A voltage V0 corresponding to an off-signal causes both the switching transistor and the reset transistor to be in an off-state that satisfies a relational expression:  $VS > V0 > VR$ . The voltage VS, the voltage VR, and the

voltage  $V_0$  may also satisfy relational expressions:  $-V_S \approx V_R$  and  $V_0=0V$ . While the switching transistors are in the on-state, the reset transistors are in the off-state, and while the reset transistors are in the on-state, the switching transistors are in the off-state. Kimura, as cited, fails to disclose any capacitive coupling.

Thus, Kimura fails to disclose or suggest a method of driving an electro-optical device, including setting the potential of the control terminal to a second voltage level by using a capacitance coupling occurring at a capacitive element connected to the control terminal, the setting of the potential of the controlling terminal to the second voltage level including applying a data voltage to the capacitor through one data line of the data lines and a switching transistor, as recited in independent claim 14.

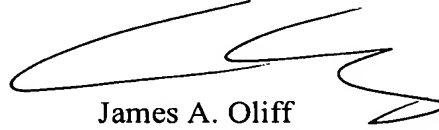
In accordance with the above remarks, independent claims 14 and 24 define patentable subject matter. Claims 15-19 depend from independent claim 14, and therefore are patentable for the same reasons as well as for the additional features recited therein. Thus, Applicant respectfully request that the rejections be withdrawn.

New claims 20-23 and 25-28 depend from independent claims 14 and 24, respectively, and therefore are patentable for the same reasons, as well as for the additional features recited therein.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 14-28 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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